

REMARKS

The Office Action mailed March 26, 2010 has been carefully studied, and the undersigned thanks the Examiner at the outset for his thorough review of the application.

The specification has been reviewed per the Examiner's suggestion, and numerous typographical errors and other inadvertent errors have been corrected. None of these amendments are believed to introduce new matter, and none are made with the intent of narrowing the scope of claim construction.

In response to the undersigned's telephonic query shortly after receipt of the Office Action concerning the expressed requirement for new drawings, the Examiner has advised that the requirement was an error. Figures 1, 3, 6 and 7 have, however, been amended to conform to the text in the specification, and substitute sheets 1/10, 2/10, 4/10 and 10/10 are accordingly submitted herewith. Again, no new matter is believed to have been introduced, and there is no intent to narrow the scope of the claims by virtue of these amendments.

The Examiner's objections under Section 112 have been carefully reviewed. It is believed that the amendments made to the claims moot most of these objections. Where not believed to be mooted, the Section 112 objection is discussed below together with the Section 102 rejection or Section 103 rejection of the claim, as the case may be.

The Examiner has initially rejected Claims 1-7 and 10 under 35 U.S.C. 102 based on U.S. Patent 6,011,992 to Hubbard (hereinafter, the '992 Patent). Claim 1 has, however, been cancelled; Claim 2 has been amended to more distinguish the claimed invention.

The '992 Patent is not believed to anticipate the claimed subject matter. For example, it is not believed that (1) it digitally processes the measurement signal to substantially offset the effects of component aging, tolerances and temperature on measurement signal accuracy (Office Action, pg. 12, first bullet point), (2) it includes means for substituting a plurality of electrical resistors in lieu of a body resistance to the amplifier for sending (*Ibid*, third bullet point), (3) it digitizes and stores in memory a plurality of measurement signals corresponding to simulated body resistance values (*Ibid*,

1 fourth bullet point), (4) it interpolates between measurement signals derived from
2 simulated body resistance values to quantity expected measurement signal values for
3 additional body resistance values (*Ibid*, fifth bullet point) or (5) forms and stores a table
4 of expected body resistance values (*Ibid*, sixth bullet point). Moreover, as discusses later,
5 the '992 Patent fails to disclose or suggest a device having other features recited in Claim
6 2.

7 Turning initially to the '992 Patent, it comprises a resistance measuring circuit 20
8 to transform the measured resistances across the living body into a measurement signal.
9 The resistance measuring circuit is best shown in Figure 3, where it comprises a voltage-
10 divider network coupled between a high voltage potential 56 and a low voltage potential
11 76. The resistance measuring circuit, itself, comprises (in series) a first resistor 58, a
12 trim adjustment potentiometer 60, a meter check circuit that places either a fixed resistor
13 68 (for calibration purposes) or the living body resistance (between electrodes 66, 64) in
14 circuit, and a third resistor 74. The effect, of course, is that the changing body resistance
15 between the electrodes causes the voltage to change at the junction of electrode 64 and
16 resistor 74 (ignoring winding 86 for the purpose of discussion).

17 (At this point, I digress for a moment to point out the effect of raising the low
18 voltage potential towards the high voltage potential. As this difference becomes minimal,
19 there is correspondingly less range of voltage change possible in representing the
20 changing body resistance; thus, the same change in body resistance causes less of a
21 measurable voltage change as the low voltage potential approaches the high voltage
22 potential. Further, changes in the measured voltage caused by RF interference and other
23 "noise" sources become more significant when the changes sought to be measured are
24 smaller. Thus, we can think in terms of two ranges of voltages at this point (which
25 become meaningful later in this description): the range in which such "noise" is not a
26 factor, and the range in which "noise" is a factor.)

27 The changing measurement signal (i.e., changing voltage at the junction of
28 electrode 64 and resistor 74) is sent as "measurement signal 88" from the output of the
29 Figure 3 circuitry to the input 126 of a first stage "summing amplifier" 34 (Figure 4A),
30 which comprises a voltage follower amplifier 124 and a first stage amplifier 138. The

1 output of amplifier 138 is fed through resistor 170 to a second stage amplifier 36
2 comprising (in part) amplifier 172.

3 The gain of the amplifier 172 is determined by three feedback circuits. First,
4 there is "high-gain circuit" 46 (Figure 4B), which is connected via line 226 in a feedback
5 arrangement to input 182 of amplifier 172. Second, there is a low-gain circuit 44 which is
6 also, but selectively, connected via line 226 to input 182 of amplifier 172. Lastly, there is
7 a digitally controlled resistance value from component 354 (Figure 4C). The "resistor"
8 may be thought of as extending between pins 5 (R+) and pin 6 (TA_ref_) of the device,
9 and one can see in Figure 4A that (1) the R+ is connected to line 226 at the top left corner
10 of the "high gain circuit" 46, and (2) the other end (TA_ref) is connected to lead 180 of
11 amplifier 172 (through resistors 162, 164).

12 Thus, the high gain and low gain circuits are coupled to the inverting input of the
13 amp, and the digitally variable resistor is coupled between the two inputs of the amplifier.

14 The '992 Patent's system is configured to switch the low-gain circuit 44 in or out,
15 depending on the value of the low voltage potential 76. As now described, the switching
16 of circuit 44 into the feedback to amplifier 172 results in a "sensitivity adjustment"; that
17 is, a given voltage difference into the amplifier results in a different voltage difference at
18 the output of the amplifier when the circuit 44 is switched in. As you will see, this
19 happens when the living body's resistance is within a certain range of resistances; i.e.,
20 there are two resistance ranges: one in which the amplifier gain is at one value, and the
21 other at which the gain is at the other value. Specifically, gain is increased for high body-
22 resistance values (those for which the low potential voltage 76 has been adjusted higher
23 in accordance with the operation of the device).

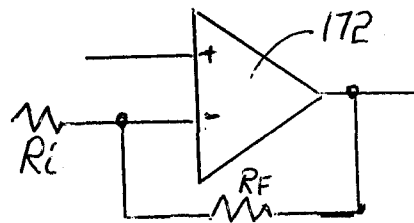
24 To review, one can see (in Figure 3) that measurement signal 88 changes if low
25 voltage potential 76 changes. During operation, the low voltage potential 76 is adjusted
26 via potentiometer 82 during operation of the device. One can see that the adjustment of
27 low voltage potential 76 upwards towards the upper voltage potential 56, results in a
28 small voltage range between the two, within which differences in body resistance must be
29 detected as very small voltage changes within that narrow range. For such small ranges,
30 gain circuit 44 is needed. Otherwise, gain circuit 46 controls the gain. Within gain

circuit 46, resistors 240, 241, 242 are always in the feedback path. The others (R234, R238) are only switched into the path when desired. This is done with switch 256 (Fig. 4B).

The gain circuit 44 is activated by the MCU 334 (Figure 4C) which sends a signal via pin 8 to the control pin CTL of latch 244 (Figure 4B). Then, the specific branch within gain circuit 44 is chosen with switch 256. The MCU activates gain circuit 44 by monitoring the value of the low voltage potential 76 at pin 14 (Figure 4C).

The patent describes the operation of the device wherein the system is initialized by adjusting the trim control 60 (Figure 3) and other circuitry so that the low voltage potential 76 is balanced for the resistor 68. A living body is then connected across the external leads 64, 66 of the resistance measuring circuit. In order to balance the circuit according to the overall resistance of the living body, the manual adjustment 78 (comprising either potentiometer 82 or remote potentiometer 94) is changed until the low voltage potential 76 achieves a balance with the overall resistance in the living body. As the manual control device 78 is adjusted, the control circuit 54 determines the gain adjustment value and signals the compensator circuit 55 to adjust the gain of the amplifier circuit.

An alternative way to view the configuration of the '992 Patent's second stage amplifier 36 (Fig. 4A) is in a simplified form; namely a simple amplifier with a feedback resistor R_f and an input resistor R_i :



The gain of this amplifier is determined by the ratio: $\frac{R_f}{R_i}$ and, as described earlier, the gain of second stage amplifier 36 is automatically controlled in response to the level of the low voltage potential 76. In the '992 Patent, the feedback resistance R_f includes one of two possible feedback paths. The "high gain" circuit 46 (Figure 4B) is

1 always connected to the second stage amplifier 36 (Figure 4A) via leads 226, 228 (which
2 are coupled to opposite sides of capacitor 230 in Figure 4B).

3 When the nominal body resistance is in a first, low range of resistances, the
4 feedback path is changed by switching in low gain circuit 44. This effectively shunts
5 high gain circuit 46.

6 In addition to adjusting the value of the R_f component, the '992 Patent's device
7 also uses an adjustment to the R_i component to obtain the needed gain changes at the
8 appropriate times. The resistance R_i includes a digitally-controlled variable resistor 354
9 (Figure 4C). This is coupled at its pin 5 (Figure 4C) to the second stage amplifier 36
10 (Figure 4A) via lead 226. More specifically, its value (designated as " R_+ " in Figure 4B
11 at the top left of flip-flop 232) is coupled by lead 226 to the inverting input of op amp
12 172 of amplifier 36.

13 The gain of the second stage amplifier 36 is continuously adjusted by the
14 automatic manipulation of the R_i and R_f values each time the operator nulls the meter
15 needle to keep the meter's needle deflection for a given body-resistance change generally
16 constant throughout the range of body resistances that can be encountered. The range of
17 possible body resistances is divided in two; namely, a first range of the relatively low
18 body resistances, and a second range of the relatively higher body resistances.

19 Within the first (low resistances) range, the gain of the second stage amplifier 36
20 is essentially determined by low gain circuit 46 in the feedback path (the " R_f " value) and
21 by MCU 334 (Figure 4C) then automatically adjusting the digitally controlled variable
22 resistor 354 to obtain the R_i value that gives the desired gain for that low voltage
23 potential. Within the second (high resistances) range, the gain of the second stage
24 amplifier is essentially determined by the high gain circuit 42 which is essentially
25 substituted into the feedback path by uncoupling the low gain circuit 46 from the
26 feedback path. The coupling and uncoupling of the low gain circuit 46 into the feedback
27 path is triggered by an enabling signal applied to the control terminal of its flip-flop 244
28 (Figure 4B) by pin 8 of MCU 334. MCU 334 does this when it detects the operative
29 level of low voltage potential 76 at its pin 14. In summary, MCU 334 monitors the low

1 voltage potential 76 and selects which of the two feedback circuits 44, 46 is the operative
2 circuit.

3 So far, the Rf value of the simplified circuit has been addresses. Turning to the Ri
4 value, the digitally controlled variable resistor 354 is controlled by MCU 334 in response
5 to the value of the low voltage potential 76 applied to its pin 14, using a “look-up table”
6 for the desired setting of the resistor.

7
8 The device defined by Claim 2 as amended is neither disclosed not suggested by
9 the ‘992 Patent, but is configured in an entirely different and novel manner. Claim 2
10 recites, for example,

11 • *means for digitizing the measurement signals corresponding to the simulated*
12 *body resistance values, and storing in memory the resulting plurality of calibrated*
13 *measurement values corresponding to the plurality of simulated body resistance values,*

14 Comment: The device of the ‘992 Patent does not digitize measurement signals
15 corresponding to simulated body resistance values. The cited portion of the ‘992 Patent
16 (col. 8, lines 15-18; col. 9, lines 14-22) are believed to refer to the lines by which the
17 MCU 334 detects whether the low gain or high gain feedback circuit is to be applied
18 across the second stage amplifier 36. While there is control of a digitally controlled
19 variable resistor 354 by the MCU, it is not believed that this meets the recited limitation
20 in Claim 2, or that this reads on the recited configuration of the claimed device.

21 • *compensation means for computing, based on the stored calibrated measurement*
22 *values, calibrated measurement values to be associated with respective additional body*
23 *resistance values*

24 Comment: It is not believed that “computing” is performed in the ‘992 Patent,
25 nor are any “stored calibrated measurement values” believed to be present.

26 • *means for producing an indicator-driving series of digital difference values*
27 *during the monitoring of the living body’s resistance that represent the difference*
28 *between the monitored living body's digitized measurement values and a selected user-*
29 *adjustable base value, the user-adjustable base value being selected by the user from*
30 *calibrated measurement values*

1 Comment: No “digital difference values” are believed to be produced during the
2 monitoring the living body’s resistance, much less “digital difference values” which
3 represent the difference between the monitored living body's digitized measurement
4 values and a selected user-adjustable base value.

5 • *means applying an automatic correcting gain factor to the indicator-driving*
6 *value as a function of the selected base value to produce the processed measurement*
7 *signal, the gain-applying means applying a first non-linear gain when the selected base*
8 *value corresponds to a very low living body resistance value of less than a first body-*
9 *resistance value, and a second non-linear gain when the selected base value corresponds*
10 *to a very high living body resistance value of more than a second body-resistance value,*
11 *the gain for the living body resistances values between said first and second values being*
12 *essentially a constant, said first non-linear gain being more than said constant and*
13 *increasing with decreasing base value, said second non-linear gain being less than said*
14 *constant and increasing with increasing base value.*

15 Comment: It is not believed that the device of the ‘992 Patent discloses or
16 suggests this.

17
18 Because the configuration and structure of the claimed device is so different than
19 that illustrated and described in the ‘992 Patent, the Examiner is requested to reconsider
20 and withdraw all rejections relying wholly or partly on the ‘992 Patent.

21 Claims 3-10 are believed allowable in that they depend directly or indirectly from
22 Claim 2 which is itself believed allowable. Discussion is accordingly omitted for the
23 sake of brevity.

24 The Examiner has contended (Office Action, pg. 13, second full paragraph) that
25 the ‘992 Patent “discloses a display that may be considered an optical encoder because
26 the display is an optical component that receives code for display and is coupled to means
27 for producing the base value as a function of the position of the manually positionable
28 means. However, an optical encoder is device that converts the rotational position of a
29 shaft to a digital code utilizing a light source and photo detector array to read an optical
30 pattern resulting from the position of a disc affixed to the shaft for rotation therewith,

1 wherein the disc typically has transparent and opaque areas that create the optical patterns
2 read by the photo detector array.

3 The Examiner has rejected Claim 10 under Section 112, first paragraph for
4 reciting “repeatedly sampling”. Although the term “repeatedly” is not explicitly used in
5 the specification, it is believed that repeated sampling is clearly disclosed to one of
6 ordinary skill in the art. For example, the specification describes (with reference to
7 Figure 3) that the meter 16 is driven by an analog output signal 480 generated by a
8 digital/analog converter 479 in response to a series of digital signals produced by a
9 central processing unit 400 that, in turn, is responsive to respective input signals 190,
10 290, 390 from front end circuitry 100, TA circuitry 20 and sensitivity circuitry 300.
11 (pg.6, lines 7-24). The front end circuitry is described as producing, during the
12 monitoring operation, a digital value indicative of the electrical resistance of the audited
13 live body. (pg. 6, lines 25-27). The resistance sensed is the instantaneous resistance
14 value, (pg. 11, lines 15-15). As described in the specification, and known to those of
15 ordinary skill in the art, the claimed device is for indicating and measuring variations in
16 the resistance of a living body. (pg. 1, lines 3-5, 9-12, 14-17, 26-33; pg, 2, lines 22-23;
17 pg. 3, line 28-31).

18 In order to indicate changes in body resistance, sampling must be repeated.
19 Otherwise, one merely has a single instantaneous resistance value. Accordingly, it is
20 submitted that “repeated sampling” is disclosed within, and supported by, the
21 specification. If the examiner believes it proper or preferable, the undersigned will
22 amend the specification to explicitly describe “repeated sampling”; it is submitted that
23 this would not entail the addition of new matter.

24 .

25 New dependant claims 11-25 are submitted herewith. These claims depend
26 directly or indirectly from Claim 2 and are believed to be allowable in that Claim 2 is
27 believed allowable.

28 It is believed that pending claims are allowable over the cited prior art, and
29 favorable consideration is requested.

The examiner is invited to telephone the undersigned if there are any matters that prevent allowance and issuance of a patent based on this application, and the examiner believes that a telephonic conference could resolve those matters more efficiently. (Please note that the undersigned is in the Pacific Time zone.)

Respectfully submitted,



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